

LIMITING CIRCUIT FOR A BRUSHLESS DC MOTOR

BACKGROUND OF THE INVENTION

1. Field of the Invention

The present invention is related to a limiting circuit for a brushless dc
5 motor. More particularly, the present invention is related to a simplified
limiting circuit for a brushless dc motor.

2. Description of the Related Art

A motor speed will be gradually reduced due to covering it with dust.
Accordingly, an electric current passing through a motor coil is rapidly risen
10 and the power consumption is thus increased. Also, as a fan wheel is
jammed, an electric current passing through the motor coil is rapidly risen
and the power consumption is thus increased. Great power consumption can
generate high temperature and heat that may result in cracks in an insulating
material of wire of the motor coil.

15 In order to prevent from passing of a greater electric current through the
motor coil during abnormal operation, there is an auto restart function built
in a drive IC. When the motor is operated abnormal, a series of pulsed
signals are used to allow a current passing through the motor coil a short-
term time, such as a few seconds, and thus a successive passage of a greater
20 electric current is prevented. After passing an electric current a short term,

the electric current on the motor coil is interrupted if an abnormal operation of the motor coil is not removed. And the drive IC produces a series of pulsed signals to restart the motor coil shortly after the interruption of the electric current on the motor coil. The auto restart function may be operated
5 repeatedly as long as the abnormal operation of the motor coil is remained. However, the auto restart function may still allow a pulsed electric current through the motor coil to thereby consume its maximum power when the motor is restarted.

Briefly, there is a need for an effective limit which can avoid great
10 power consumption and high temperature when the motor speed is reduced or the fan wheel is jammed.

The present invention intends to provide a limiting circuit for a brushless dc motor, which includes a simplified limiting circuit connected to a motor drive circuit to thereby limit an electric current supplied from a
15 power source in such a way to mitigate and overcome the above problem.

SUMMARY OF THE INVENTION

The primary objective of this invention is to provide a limiting circuit for a brushless dc motor, which includes a simplified limiting circuit connected to a motor drive circuit to thereby limit a current supplied from a
20 power source. Consequently, it may prolong useful life of the motor.

The secondary objective of this invention is to provide the limiting circuit for a brushless dc motor, which includes a simplified limiting circuit to thereby simplify the entire structure of the motor.

The limiting circuit for a brushless dc motor in accordance with the present invention comprises a first transistor, a second transistor, a first resistor and a second resistor. The first transistor, the second transistor, the first resistor and the second resistor are arranged in complementary connection to constitute the limiting circuit which has a first terminal connected to a power source and a second terminal connected to a motor drive circuit.

Other objectives, advantages and novel features of the invention will become more apparent from the following detailed description and the accompanying drawings.

BRIEF DESCRIPTION OF THE DRAWINGS

The present invention will now be described in detail with reference to the accompanying drawings herein:

FIG. 1 is a schematic circuitry of a limiting circuit for a brushless dc motor in accordance with a first embodiment of the present invention;

FIG. 2 is a schematic circuitry of a limiting circuit for a brushless dc motor in accordance with a second embodiment of the present invention;

FIG. 3 is a schematic circuitry of a limiting circuit for a brushless dc motor in accordance with a third embodiment of the present invention; and

FIG. 4 is a schematic circuitry of a limiting circuit for a brushless dc motor in accordance with a fourth embodiment of the present invention.

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DETAILED DESCRIPTION OF THE INVENTION

Referring now to the drawings, there are four embodiments of the present invention shown therein, which include generally a motor drive circuit member and a limiting circuit member.

Referring initially to FIG. 1, it illustrates a schematic circuitry of a limiting circuit for a brushless dc motor in accordance with a first embodiment of the present invention.

Referring again to FIG. 1, a limiting circuit 10 in accordance with the first embodiment is connected with a power source (Vcc) 1 and a motor drive circuit 2. The limiting circuit 10 includes a first transistor 11, a second transistor 12, a first resistor 13 and a second resistor 14. The limiting circuit 10 has a first terminal (a) connected to the power source 1, and a second terminal (b) connected to the motor drive circuit 2. The first resistor 13 serves as the first terminal (a) while a base of the first transistor 11 serving as the second terminal (b). Preferably, the first transistor 11 and the second transistor 12 are NPN type transistors which serve as switches.

Referring again to FIG. 1, a series connection of the first transistor 11 with the first resistor 13 and a series connection of the second transistor 12 with the second resistor 14 are arranged in complementary connection to constitute the limiting circuit 10. The first resistor 13 has a first end
5 connected to the power source 1, and a second end connected to a base of the first transistor 11 and a collector of the second transistor 12. Meanwhile, the second resistor 14 has a first end connected to an emitter of the first transistor 11 and a base of the second transistor 12, and a second end connected to the ground. In operation, the first transistor 11 serves as a first
10 switch connected to the motor drive circuit 2 and the second transistor 12 serves as a second switch adapted to control the first transistor 11 for turning on or off.

Referring again to FIG. 1, when the motor drive circuit 2 is operated in normal, the power source 1 supplies with a bias through the first resistor 13
15 to the base of the first transistor 11. As the bias of the base of the first transistor 11 has risen greater than a predetermined voltage, the first transistor 11 is saturated. Since the power source 1 supplies an electric current with a normal voltage to pass through the motor drive circuit 2, the first transistor 11 and the second resistor 14 successively, an end point (c) of
20 the second resistor 14 provides with a bias that is inadequate to turn on the

second transistor 12. Consequently, the limiting circuit 10 is unable to conduct an electric current from the power source 1 to the ground through the first resistor 13 and the second transistor 12. Alternatively, a completed electric current supplied from the power source 1 flows through the motor drive circuit 2, the first transistor 11 and the second resistor 14 successively.

Referring again to FIG. 1, when the motor drive circuit 2 is operated in abnormal or jammed, an electric current supplied from the power source 1 is risen rapidly. Since the power source 1 supplies a greater electric current with a high voltage to pass through the motor drive circuit 2, the first transistor 11 and the second resistor 14 successively, an end point (c) of the second resistor 14 provides with a bias that is risen rapidly and adequate to turn on the second transistor 12. Once the bias of the end point (c) of the second resistor 14 is risen greater than a predetermined voltage, the second transistor 12 is saturated and then a base of the first transistor 11 has a decrease in voltage. When the bias of the base of the first transistor 11 is lower than a predetermined voltage, the first transistor 11 is returned to turn off. Consequently, the limiting circuit 10 is completely connected an electric current from the power source 1 to the ground through the first resistor 13 and the second transistor 12. Thereby, a greater electric current supplied from the power source 1 cannot flow through the motor drive circuit 2, the

first transistor 11 and the second resistor 14 successively, and the motor drive circuit 2 is cut off.

Referring again to FIG. 1, as the first transistor 11 is turned off, the bias of the base of the second transistor 12 and the end point (c) of the second resistor 14 may be decreased rapidly. Once the bias of the base of the second transistor 12 is lower than a predetermined voltage, the second transistor 12 is turned off. In this circumstance, since the second resistor 12 is turned off, the bias of the base of the first transistor 11 is risen again. Once the bias of the base of the first transistor 11 is greater than a predetermined voltage, the first transistor 11 is turned on again.

Referring again to FIG. 1, when the first transistor 11 is turned on again, the bias of the end point (c) of the second resistor 14 is also able to turn on the second transistor 12 again if the electric current supplied from the power source is still high. Meanwhile, a bias of an end point (d) of the first resistor 13 is synchronously decreased to turn off the first transistor 11. Consequently, the limiting circuit 10 is completely reconnected an electric current from the power source 1 to the ground through the first resistor 13 and the second transistor 12. Therefore, the limiting circuit 10 actuates the first transistor 11 and the second transistor 12 repeatedly until the electric current and the voltage of the power source 1 are stable and normal. Namely,

the operation of the motor drive circuit 2 is returned to normal state.

Referring to FIGS. 2 through 4, reference numerals of the second through fourth embodiments of the present invention have applied the identical numerals of the first embodiment. The construction of the limiting
5 circuit in accordance with the second through fourth embodiments of the present invention have similar configuration and same function as that of the first embodiment and the detailed descriptions may be omitted.

Turning to FIG. 2, it illustrates a schematic circuitry of a limiting circuit for a brushless dc motor in accordance with a second embodiment of the
10 present invention.

Referring to FIG. 2, a limiting circuit 10 in accordance with the second embodiment is connected with a power source (Vcc) 1 and a motor drive circuit 2. The limiting circuit 10 includes a first transistor 11, a second transistor 12, a first resistor 13 and a second resistor 14. In comparison with
15 the first embodiment, each of the first transistor 11 and the second transistor 12 of the second embodiment adopts a P type field-effect transistor instead of a NPN type transistor.

Turning to FIG. 3, it illustrates a schematic circuitry of a limiting circuit for a brushless dc motor in accordance with a third embodiment of the
20 present invention.

Referring to FIG. 3, a limiting circuit 10 in accordance with the third embodiment is connected with a power source (Vcc) 1 and a motor drive circuit 2. The limiting circuit 10 includes a first transistor 11, a second transistor 12, a first resistor 13 and a second resistor 14. The limiting circuit 10 has a first terminal (a) connected to the power source 1, and a second terminal (b) connected to the motor drive circuit 2. An emitter of the first transistor 11 serves as the first terminal (a) while a collector of the second transistor 12 serving as the second terminal (b). Preferably, the first transistor 11 and the second transistor 12 are PNP type transistors which serve as switches.

Referring again to FIG. 3, in operation, the second transistor 12 serves as a first switch connected to the motor drive circuit 2 and the first transistor 11 serves as a second switch adapted to control the second transistor 12 for turning on or off.

Referring again to FIG. 3, when the motor drive circuit 2 is operated in normal, the first transistor 11 is turned off and the second transistor 12 is turned on. Consequently, a completed electric current supplied from the power source 1 flows through the second resistor 14, the second transistor 12 and the motor drive circuit 2 successively.

Referring again to FIG. 3, when the motor drive circuit 2 is operated in

abnormal or jammed, an electric current supplied from the power source 1 is risen rapidly. As the greater electric current is passed through the second resistor 14, a bias of an end point (c) of the second resistor 14 is constantly decreased and adequate to turn on the first transistor 11. Once the bias of the
5 end point (c) of the second resistor 14 is decreased lower than a predetermined voltage, the first transistor 11 is saturated and then a base of the second transistor 12 has an increase in voltage. When the bias of the base of the second transistor 12 is greater than a predetermined voltage, the second transistor 12 is returned to turn off. Consequently, the limiting circuit
10 is completely connected an electric current from the power source 1 to the ground through the first transistor 11 and the first resistor 13. Thereby, a greater electric current supplied from the power source 1 cannot flow through the second resistor 14, the first transistor 11 and the motor drive circuit 2 successively, and the motor drive circuit 2 is cut off.

15 Referring again to FIG. 3, as the second transistor 12 is turned off, the bias of the base of the first transistor 11 and the end point (c) of the second resistor 14 may be increased rapidly. Once the bias of the base of the first transistor 11 is greater than a predetermined voltage, the first transistor 11 is turned off. In this circumstance, since the first resistor 11 is turned off, the
20 bias of the base of the second transistor 12 is decreased again. Once the bias

of the base of the second transistor 12 is lower than a predetermined voltage,
the second transistor 12 is turned on again.

Referring again to FIG. 3, when the second transistor 12 is turned on
again, the bias of the end point (c) of the second resistor 14 is also able to
5 turn on the first transistor 11 again if the electric current supplied from the
power source is still high. Meanwhile, a bias of an end point (d) of the first
resistor 13 is synchronously increased to turn off the second transistor 12.
Consequently, the limiting circuit 10 is completely reconnected an electric
current from the power source 1 to the ground through the first transistor 11
10 and the first resistor 13. Therefore, the limiting circuit 10 actuates the first
transistor 11 and the second transistor 12 repeatedly until the electric current
and the voltage of the power source 1 are stable and normal. Namely, the
operation of the motor drive circuit 2 is returned to normal state.

Turning to FIG. 4, it illustrates a schematic circuitry of a limiting circuit
15 for a brushless dc motor in accordance with a fourth embodiment of the
present invention.

Referring to FIG. 4, a limiting circuit 10 in accordance with the fourth
embodiment is connected with a power source (Vcc) 1 and a motor drive
circuit 2. The limiting circuit 10 includes a first transistor 11, a second
20 transistor 12, a first resistor 13 and a second resistor 14. In comparison with

the third embodiment, each of the first transistor 11 and the second transistor 12 of the second embodiment adopts a N type field-effect transistor instead of a PNP type transistor.

Although the invention has been described in detail with reference to its
5 presently preferred embodiment, it will be understood by one of ordinary skill in the art that various modifications can be made without departing from the spirit and the scope of the invention, as set forth in the appended claims.